

Customer No.: 31561
Application No.: 10/604,821
Docket No.: 11208-US-PA

In The Abstract

Please replace the abstract with the following amendments to abstract.

~~A power detector and a power detecting method are presented in the embodiment of the present invention. Equipped with the power detector or exploiting the power detecting method, an integrated circuit (IC) will be able to automatically self-configure a data power level of the IC effectively and inexpensively. The embodiment of the present invention is particularly useful to a Dynamic Random Access Memory (DRAM) data power auto-configuration so that a data power level of the DRAM need not to be preset during a DRAM fabrication process.~~

A power source detecting circuit of a Dynamic Random Access Memory (DRAM) and an operating method thereof comprise a first power source detector to operate at a good power level, and a second power source detector to operate at an appropriate data power level. The first power source detector receives a first external power, and outputs a signal to indicate whether the first external power is ready for the DRAM to function properly. The second power source detector receives a second external power, and provides a first output state to indicate that the DRAM data power is at a first data power level and a second output state to indicate that the DRAM data power is at a second data power level. After the DRAM receives the data power level indication from the second power source detector, the DRAM automatically configures itself to operate at the corresponding data power level.